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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/421,217	10/20/1999	HIDEKI TAKAHASHI	0057-2533-2Y	3815

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11/06/2002

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EXAMINER

LOKE, STEVEN HO YIN

ART UNIT

PAPER NUMBER

2811

DATE MAILED: 11/06/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/421,217

Applicant(s)

TAKAHASHI, HIDEKI

Examiner

Steven Loke

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 16 October 2002.
- 2a) ☒ This action is FINAL. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 22-26 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 22-26 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other: _____

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1. The finality of the last action mailed on 7/30/02 is withdrawn in view of the present final office action. The terminal disclaimer and the amendment filed on 10/16/02 have been entered.
2. Claims 22-26 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Fig. 3 discloses two insulated gate electrode [49] and at least four channel regions formed in the p-type layer [44] in the insulated gate semiconductor device. The specification never discloses the portion of the fourth semiconductor layer forms a channel region as an only channel region of the insulated gate semiconductor device as claimed in claim 22.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

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4. Claims 22, 24 and 26 insofar, as in compliance with 35 USC 112, are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Kinzer.

In regards to claim 22, Kinzer shows all the elements of the claimed invention in fig. 19. It is an insulated gate semiconductor device, comprises: a first semiconductor layer [50] of a first conductivity type (p) having first and second main surfaces on opposite sides thereof; a second semiconductor layer [52] of a second conductivity type (n) provided on the first main surface of the first semiconductor layer; a third semiconductor layer [61] of the second conductivity type higher in an impurity concentration (n+) and thinner than the second semiconductor layer [52], and provided on a surface of the second semiconductor layer [52]; a fourth semiconductor layer [81] of the first conductivity type provided on a surface of the third semiconductor layer [61], wherein the third semiconductor layer [61] is interposed between the second semiconductor layer [52] and a bottom of the fourth semiconductor layer [81] and is in direct contact with said second semiconductor layer [52]; a fifth semiconductor layer [131] of the second conductivity type selectively provided in a surface of the fourth semiconductor layer [81] and opposing the third semiconductor layer [61] through the fourth semiconductor layer [81]; a first main electrode [160] disposed across and connected with surfaces of the fourth and fifth semiconductor layers [81, 131]; a second main electrode [170] provided on the second main surface of the first semiconductor layer [50]; an insulating film [95] provided on a portion of the fourth semiconductor layer [81] interposed between the third and fifth semiconductor layers [61, 131]; a control

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electrode [114] facing the portion through the insulating film [95] so that the portion forms a channel region.

In regards to claim 24, Kinzer further discloses a sixth semiconductor layer [51] of the second conductivity type higher in an impurity concentration (n+) than the second semiconductor layer [52] provided between the first and second semiconductor layers [50, 52].

In regards to claim 26, Kinzer further discloses the first main electrode [160] is not contacting any other semiconductor than the fourth and fifth semiconductor layers [81, 131].

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 23 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kinzer.

In regards to claim 23, Kinzer differs from the claimed invention by not showing the second semiconductor layer extends through the first semiconductor layer and is partially exposed in the second main surface of the first semiconductor layer. It would have been obvious for the second semiconductor layer extends through the first semiconductor layer and is partially exposed in the second main surface of the first semiconductor layer because it depends to the switching speed of the device.

In regards to claim 25, Kinzer differs from the claimed invention by not showing the sixth semiconductor layer extends through the first semiconductor layer and is partially exposed in the second main surface of the first semiconductor layer. It would have been obvious for the sixth semiconductor layer extends through the first semiconductor layer and is partially exposed in the second main surface of the first semiconductor layer because it depends to the switching speed of the device.

7. Applicant's arguments with respect to claims 22-26 have been considered but are moot in view of the new ground(s) of rejection.

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

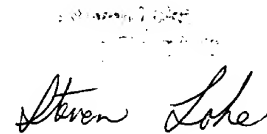
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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (703) 308-4920. The examiner can normally be reached on 7:50 am to 5:20 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

sl
November 4, 2002


Steven Loke